

ABSTRACT

1 A virtual input/output (I/O) interconnect mechanism, and a corresponding method,
2 for use in a computer system having a plurality of I/O devices and a plurality of
3 processing units, where I/O devices and processing units are coupled by one or more
4 bridge units, includes an address decode block having a multiplexer that multiplexes
5 inputs to produce an address, where the address relates to a transaction related to a
6 processor unit, a range register decoder that receives the address and provides a
7 destination address of a module to receive the transaction related to the address, and a
8 reroute module identification block that receives the destination address. The reroute
9 module identification block, includes an original module identification that provides an
10 address of one or more original modules in the computer system, and a remapped module
11 identification that provides logical destination module identifications of substitute
12 modules in the computer system, where a substitute module replaces functions of an
13 original module in the computer system.